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pcim

6 – 8.5.2025
NUREMBERG, GERMANY

**CONFERENCE
AND SEMINAR
PROGRAM**

Messe Frankfurt Group

Seminars

Sunday, 4 May 2025

2 p.m. – 5.30 p.m.

Safeguarding Industrial Interfaces: Reliable Protection against Transients with Modern TVS Diodes

Heinz Zenkner, Würth Elektronik eiSos, DE

Basics for Electromagnetic Compatibility (EMC) of Power Electronics

Jacques Laeuffer, Dtalents, FR

Enhancing Power Quality & Efficiency with Multi-Pulse Converters: Design, Implementation, Trends

Kapila Warnakulasuriya, University of Teesside, GB

Advanced Power Factor Control Techniques, from Analog to Digital Control: Theory and Practice

Francesco Gennaro, Claudio Adragna, STMicroelectronics, IT

Development of Superjunction SiC MOSFETs: Challenges and Milestones

Rana Riahi, Microchip Technology, DE

From Fundamentals to Frontiers: Comprehensive Training on SiC Applications, Trends, and Design

Adam Anders, Austin Curbow, Yuequan Hu, Chen Wei, Jianwen Shao, Wolfspeed, US

Monday, 5 May 2025

9 a.m. – 12.30 p.m.

Magnetics for High Power and Very High-Density in Artificial Intelligence and Modern Automotive

Ionel Dan Jitaru, Rompower, US

A New Design Approach for Faster Power Electronics:

Part 1: Propagation, Commutation & Converters

Jacques Laeuffer, Dtalents, FR

Device Design and Driving of Power Semiconductor Devices (Part 1)

Thomas Basler, Chemnitz University of Technology, DE

Jan Fuhrmann, University of Rostock, DE

Controlling Power Converters at the Limits of Physics: Modelling and Design (Part 1)

Christian Dick, Cologne University of Applied Science, DE

Christoph van der Broeck, RWTH Aachen University, DE

Sebastian Richter, AixControl, DE

Design, Testing, and Reliability Prediction of Power Trains for Modern Automotive Applications

Francesco Iannuzzo, Radu Bojoi, Gianmario Pellegrino,

Polytechnic University of Turin, IT

Switching Losses in Power Semiconductors (Part 1)

Sebastian Sprunck, Marco Jung, Fraunhofer IEE, DE

Hauke Lutzen, University of Bremen, DE

Christian Lottis, Bonn-Rhein-Sieg University of Applied Sciences, DE

Efficient GaN Power Electronics: Improved Static and Dynamic Performances, and Optimal Cooling

Elison Matioli, EPFL, CH

DC Grid: Topology, Converters, Control and Protection, a Hands-On Experience

Peter van Duijsen, Diego Zuidervliet, The Hague University of Applied Sciences, NL

Monday, 5 May 2025

1.30 p.m. – 5 p.m.

High Power and Very High-Density Technologies for Artificial Intelligence and Modern Automotive

Ionel Dan Jitaru, Rompower, US

A New Design Approach for Faster Power Electronics:

Part 2: Magnetics, Capacitors, SMPS & Drives

Jacques Laeuffer, Dtalents, FR

Power Cycling and Lifetime Estimation of Power Semiconductor Devices (Part 2)

Thomas Basler, Chemnitz University of Technology, DE

Jan Fuhrmann, University of Rostock, DE

Controlling Power Converters at the Limits of Physics: Digital Design and Implementation (Part 2)

Christian Dick, Cologne University of Applied Science, DE

Christoph van der Broeck, RWTH Aachen University, DE

Sebastian Richter, AixControl, DE

Innovative Product Development for Power Electronics: Tools, AI and Customer-Centered Solutions

Aaron Hutzler, Bond Pulse, DE

Björn Noreik, BNB-Qualitätsstatistik und Training, DE

Switching Losses in Power Semiconductors (Part 2)

Sebastian Sprunck, Marco Jung, Fraunhofer IEE, DE

Hauke Lutzen, University of Bremen, DE

Christian Lottis, Bonn-Rhein-Sieg University of Applied Sciences, DE

Wireless Chargers for Electric Vehicle (EV) Applications – Opportunities, Challenges and Solutions

Miroslav Vasic, Nikola Mirkovic, Polytechnic University of Madrid, ES

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Conference

Tuesday, 6 May 2025

Keynote: Medium Voltage Power Electronics Building Blocks for Future Electronic Energy Networks

Dushan Boroyevich, CPES, US

Oral Sessions:

SiC MOSFET	Packaging Concepts	Converter Design Optimisation	Modeling and Simulations I	Monitoring and Testing	Virtual Prototyping	Morning
High Power Converters	Hybrid Device Concepts	Condition Health Monitoring	Package Reliability	Pulse Width Modulation	Measurement Methods	Afternoon

+ 16 Poster / Dialogue Sessions with more than 115 Poster Presentations

Wednesday, 7 May 2025

Keynote: Monolithically Integrated Bi-Directional Switch (BDS) GaN HEMT Technology

Michael Harrison, Enphase Energy, US

Oral Sessions:

Power Electronics for E-Mobility II	SiC Robustness	Materials for Thermal Management	Inductors & Transformers Design	Intelligent Gate Drivers	Innovative Drive Systems for E-Mobility	Morning
Converter Design for E-Mobility	Reliability in Power Electronics	Advanced Cooling Technology	Paralleling Devices	Server Power Supplies	Power Electronics for E-Mobility III	Afternoon

+ 16 Poster / Dialogue Sessions with more than 115 Poster Presentations

Thursday, 8 May 2025

Keynote: Challenges of Green Growth – Limited Energy Return on Energy Invested & Critical Raw Material Shortage

Johann Walter Kolar, ETH Zurich, CH

Oral Sessions:

Special Session: Circular Economy in Power Electronics	GaN Devices II	Control Methods	Inverter Design and Reliability	High-Power Modular Converters	Morning
IGBT Technologies	Smart Battery and DC Grids	High Frequency Converters	Sintering Technology	Special Passives Design	Afternoon

+ 10 Poster / Dialogue Sessions with more than 70 Poster Presentations

Conference Sponsors



Full program available on
pcim.mesago.com/program

As of January 2025/
subject to changes.

Reasons to attend

Be an important part of the power electronics community.

01

Gain a holistic view of the **power electronics value chain**.

02

Attain access to **more than 450 presentations** on the latest research, developments and technologies on site.

03

Visit **more than 600 exhibitors** showcasing their latest innovations in Nuremberg.

04

Benefit from manifold **opportunities to network** with experts from academia and industry for the longer term.



» The PCIM brings that unique blend of industry and academia, which we don't really find in many different conferences. It's really an extremely important part of the training of our future generations of researchers and engineers. «

Drazen Dujic
PCIM Conference Board of Directors
Power Electronics Laboratory, EPFL, Switzerland

Call for Papers

Are you an expert in the field of power electronics and would you like to present your latest developments and research findings to a high-qualified audience from academia and industry?

Take the chance and become a speaker
at the PCIM Conference from 9 – 11 June 2026!

Call for Paper Deadlines

Submission of abstracts

15 October 2025

Notification of acceptance

January 2026

Submission of full manuscript

7 April 2026

The Call for Papers will be online from July 2025.

Publishing of your paper in the PCIM Conference proceedings as well as databases like IEEE Xplore.

Venue

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