



ISPSD'12 - Call for Papers

The 24th IEEE International Symposium on Power Semiconductor Devices and ICs
June 3-7, 2012, Bruges, Belgium

ISPSD is the premier forum for technical discussion in all areas of power semiconductor devices and power integrated circuits. ISPSD'12 will be held in the beautiful city of Bruges, Belgium, a UNESCO World Heritage Site, also known as 'the Venice of the North'. The medieval city is unique: the look and the feel of the Middle Ages still are present in the streets and buildings.

Topics of interest include but are not limited to :

- **Devices:** Device Physics, Device Design, High Frequency Devices, High Power Devices, Smart Power Devices, Safe-Operating Area, Reliability, ESD
- **Processes:** Process Integration, Lifetime Control, Passivation, Crystal Growth, III-V (hetero)-epitaxial growth
- **Modeling/Simulation:** Device and Circuit Simulation, Layout, Verification Tools
- **Materials:** Si, GaAs, SiC, GaN, Diamond
- **Power ICs:** Isolation Techniques, SOI, Circuit Design, Device Technology, Energy Capability and SOA, Reliability, ESD, Power SoC, Monolithic vs Hybrid
- **Packaging:** Novel Packaging Concepts, Power SiP, Stress and Thermal Analysis, Thermal Management
- **Applications:** Hybrid Vehicles, Power Supplies, Computer and Telecom Power, Motor Drives, Utility

Abstract Submission

- Prospective authors should visit the ISPSD'12 website: www.ispsd2012.com
- A PDF abstract should be submitted through this website including:
 - A single-page text summary in English (500 words maximum)
 - Up to two additional pages of supporting graphs and tables
 - The abstract heading must include: Title, Authors, Affiliations, Address, Phone, Fax, Email
 - The abstract must CLEARLY state: Purpose of work, How the work advances prior art, Specific results and their significance, up-to-date references
 - Mark the paper's eligibility for Best Student Paper Award during submission (papers with a student as the first author and to be presented by the same student)
- **Abstract submission deadline is OCTOBER 17, 2011**
- Author notification will be sent out by December 19, 2011
- Final manuscripts must be submitted before March 15, 2012

General Chair: Dr. Peter Moens, ON Semiconductor, Email: peter.moens@onsemi.com
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