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Mixed-signal IC verification Engineer (11775685)

Posting Date Feb 4, 2011

Primary Location FR-Midi-Pyrénées-Haute-Garonne-Toulouse (ZFR11)

Job Validation and Verification

Education Level Master's Degree

Job Type Experienced

Is It Temporary Contract?: No

Description

Company description:

Freescale Semiconductor is the global leader in embedded processing solutions, supporting the automotive, consumer, industrial and networking markets. From microprocessors and microcontrollers to sensors, analog ICs and connectivity — our vital technologies are the foundation to the innovations that make our world greener, safer, healthier and more connected. For more than 50 years, Freescale has played an essential role in the evolution of embedded solutions, and we want you to be a part of our next half-century. Freescale is committed to attracting great people of all styles, thoughts, cultures and backgrounds. We are driven by a culture of ownership, teamwork, and results — an engagement worldwide of more than 20,000 employees.

Department description:

The Analog Mixed signal and Power Division (AMPD) of Freescale focuses on high performance analog electronic solutions for the automotive sector.

The AMPD Europe Design Center, based in Toulouse, produces "Smartpower ICs" with highly integrated solutions for various applications involving analog, analog-digital and power electronics.

The design center consists of a group of more than 40 Design Engineers who are highly experienced in analog and mixed analog-digital design.

Scope of Responsibilities/Expectations:

Within this group, you are responsible for the design verification complex mixed-signal integrated circuits for highly renowned international car manufacturers and equipment suppliers; each of which having their own unique cultures and work practices.

You have responsibility to drive, improve and standardize methodology of SoC verification with an overall objective: "first silicon pass success and 0 ppm failure rate in production".

Accordingly to this methodology and with support of design team, you coordinate the execution and validation of the digital, analog and mixed-signal behavioral model and simulation test benches.

You lead the top-cell validation plan to guarantee the verification coverage and to decide when the IC is ready for tape-out.

In collaboration with customer and application team, you define and validate the use case on silicon.

You actively contribute to the lesson-learn close loop process.

You contribute to standardize procedure, requirements, deliverables and documentation relative to the verification flow.

In team work, you support DFT strategy and participate to the definition of ATE and Lab validation plan.

In midterm perspective, you will deploy this SoC verification flow to our WW design centers.

Qualifications:

Requirements:

Proven experience, 5+ years, in SoC frontend verification,

Competences in Analog and Digital micro-electronic,

Verilog RTL and Gate level, VerilogAMS,

Applicant should have efficient debugging skills and familiarity with major debug tool vendors.

The position requires continuous contact with the customer; good communications, including English skills, are therefore imperative.

The ability to work in a team is a mandatory.

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